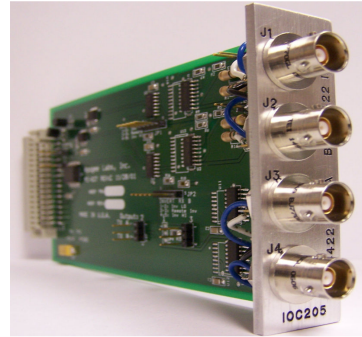


AL2073 INTERFACER PRODUCT LINE

IOC205
RS-422
BUFFER
DISTRIBUTION
MODULE**FEATURES**

- Two Independent RS-422 Inputs
- Two Independent RS-422 Outputs
- Drives Daisy Chain Bus and/or Global Bus
- Output Polarity Control
- Outputs are jumper selectable to either input
- High Current Outputs meet TIA/EIA-422-B requirement

OVERVIEW

The IOC205 accepts two RS-422 signals and buffers them to two RS-422 outputs. Output signal can be sourced from either input signal creating a 1 in to 2 output buffer driver. Each input has a polarity control jumper that controls the polarity of the input signal. The IOC204 uses four triax connectors, two for the input and two for the output and operates up to 35 kbps. It drives the Daisy Chain bus and can be configured to drive the Global Bus providing additional signal duplication capability. The IOC205 requires one slot of the 14 available slots in the Model 2073 chassis.

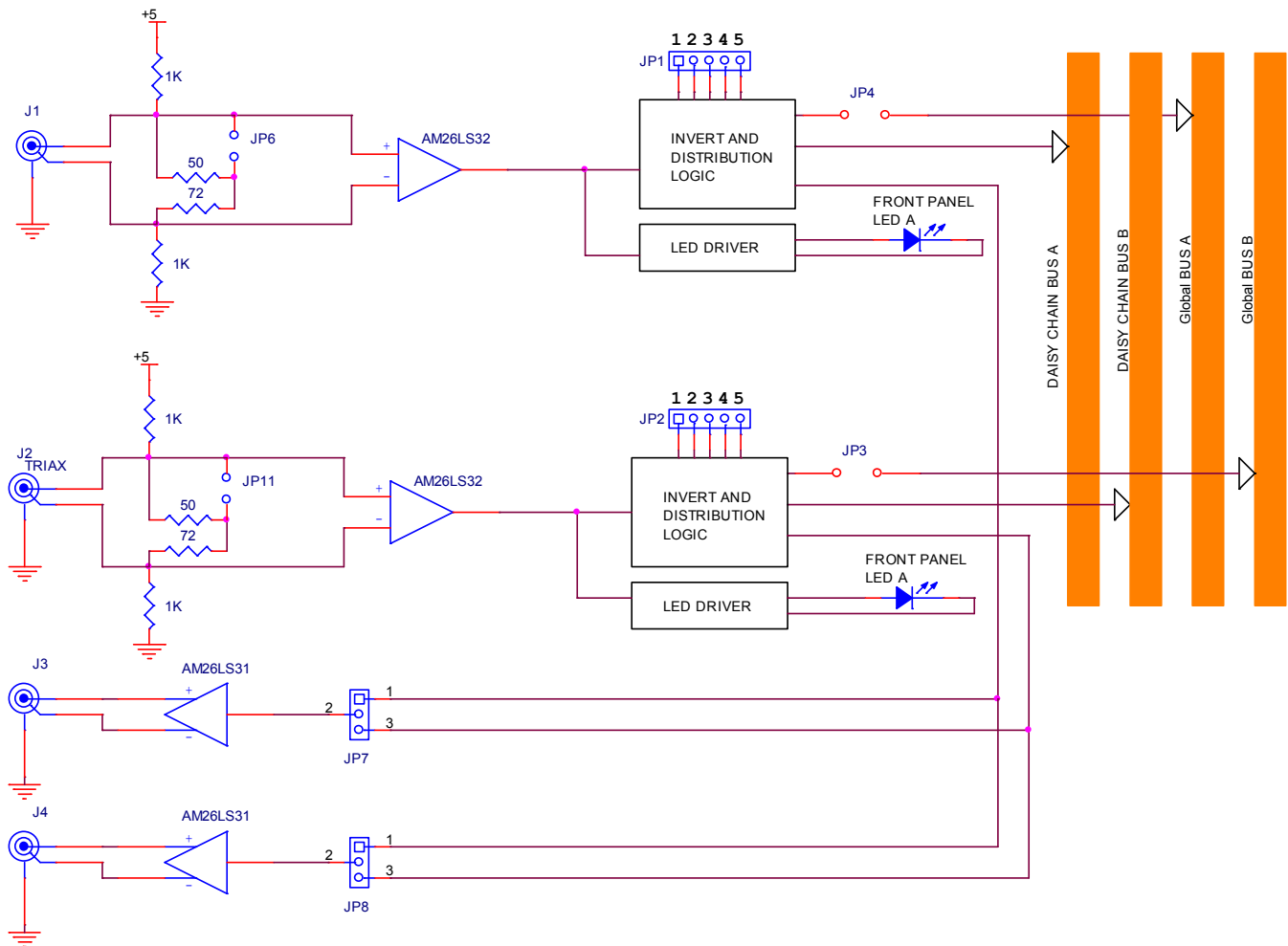


Figure 1: Model IOC205 Block Diagram

SPECIFICATIONS

GENERAL

- 2 Independent RS-422 Input Channels
- 2 Independent RS-422 Output Channels
- Single Slot Module (3" x 6" x 0.9")
- AL2073 Pluggable Interface Module

INPUT:

- 2 each -Triax (Trompeter BJ-77)
- Selectable input termination

Output:

- 2 each -Triax (Trompeter BJ-77)
- RS-422 levels TIA/EIA-422-B

APPLICATION INFORMATION

The IOC205 is used to receive and terminate 2 each RS-422 signals and buffer/drive the inputs to RS-422 TIA/EIA-422-B standard outputs. The IOC205 can be used as part of a RS-422 distribution application where the daisy chain bus or global bus is used to move the input signals to output buffer cards creating a 2 input to N-output configuration. See Table 1 for configuration information. The 2073 can house up to 14 of these type modules.

This module can also be plugged into Apogee Models:

- 2907 and 2908: Data Acquisition Mux/Demux
- 6801: 5 Channel BERT Operation
- 6804: Multi Channel Clock Recovery Unit

Apogee Labs Inc. products are sold by description only. Apogee Labs Inc. reserves the right to make changes in circuit design, software, hardware and/or specifications at any time without notice. Although Apogee Labs Inc. believes that the information provided is current and accurate, Apogee Labs Inc. does not assume any responsibility or liability for the use of any product described. It is the responsibility of the user to determine appropriate use of the product in any given application.

| Jumper | Assignment |
|--------|---|
| JP1 | J1 Input Inversion Control – Short 1-2 Not Invert, Short 4-5 Invert |
| JP2 | J2 Input Inversion Control – Short 1-2 Not Invert, Short 4-5 Invert |
| JP3 | J2 Inputs connects to Global Bus B – Short: Enable, Open: Disable |
| JP4 | J1 Inputs connects to Global Bus A – Short: Enable, Open: Disable |
| JP5 | J1 Input Termination – Short: 75 Ohm, Open: 120 Ohm |
| JP6 | J2 Input Termination – Short: 75 Ohm, Open: 120 Ohm |
| JP7 | Output J3 Data Source – J1 Input Short 1-2, J2 Input Short 2-3 |
| JP8 | Output J4 Data Source – J1 Input Short 1-2, J2 Input Short 2-3 |

Table 1